



Practitioner's Docket No. TSL-105

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

re application of: Ishai Nachumovksy

Application No.: 09/975,049

Group No.: 2814

Filing Date: 10/10/2001

Examiner: Howard Weiss

For: TWO-BIT SPLIT-GATE NON-VOLATILE MEMORY TRANSISTOR

Assistant Commissioner for Patents
Washington, D.C. 20231

#5/82T(1)
10/30/02 Hays
PATENT

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PETITION AND FEE FOR EXTENSION OF TIME

(37 C.F.R. § 1.136(a))

1. This is a petition for an extension of the time for a total period of ONE month(s) up to and including October 14, 2002.
2. A response in connection with the matter for which this extension is requested is filed herewith. The response is the filing of a **Response To First Office Action**.
3. Applicant is a LARGE entity.
4. Calculation of extension fee (37 C.F.R. § 1.17(a)(1)-(5)):
Extension: Fee for LARGE entity:
ONE month(s) \$110.00
Fee \$110.00
If an additional extension of time is required, please consider this a petition therefor.
5. Extended period for response: Based on the extension requested in this petition (and that for which a previous petition has been filed, if any), the extended period for response will expire on October 14, 2002.
6. Fee Payment: Authorization is hereby made to charge the amount of \$110.00 to Deposit Account No. 50-0574. Charge any additional fees required by this paper or credit any overpayment in the manner authorized above. A duplicate copy of this paper is attached.

Date: October 14, 2002

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I hereby certify that this correspondence is being deposited with the United States Postal Service as FIRST CLASS mail, in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C., 20231, on October 14, 2002.

Oct. 14, 2002
Date

Carrie Reddick
Signature

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